

2STC5948

High power NPN epitaxial planar bipolar transistor

Features

- High breakdown voltage V_{CEO} = 250 V
- Complementary to 2STA2120
- Typical f_t = 25 MHz
- Fully characterized at 125 °C

Application

Audio power amplifier

Description

The device is a NPN transistor manufactured using new BiT-LA (Bipolar transistor for linear amplifier) technology. The resulting transistor shows good gain linearity behaviour.

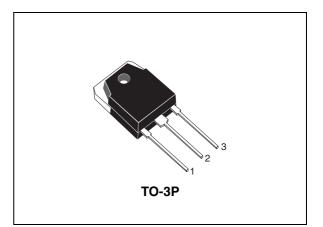
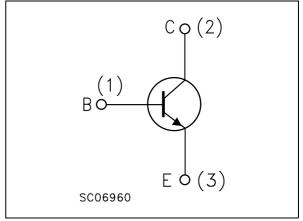


Figure 1. Internal schematic diagram



Order code	Marking	Package	Packaging
2STC5948	2STC5948	TO-3P	Tube

1 Electrical ratings

Table 2. Absolute maximum rating

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage ($I_E = 0$)	250	V
V _{CEO}	Collector-emitter voltage $(I_B = 0)$	250	V
V _{EBO}	Emitter-base voltage ($I_C = 0$)	6	V
۱ _C	Collector current	17	А
I _{CM}	Collector peak current (t _P < 5 ms)	34	А
P _{TOT}	Total dissipation at $T_c = 25 \text{ °C}$	200	W
T _{stg}	Storage temperature	-65 to 150	°C
Т _Ј	Max. operating junction temperature	150	°C

Table 3.Thermal data

Symbol	Parameter		Value	Unit
R _{thj-case}	Thermal resistance junction-case	max	0.625	°C/W



2 Electrical characteristics

($T_{case} = 25 \ ^{\circ}C$; unless otherwise specified)

Table 4.	Electrical	characteristics

Symbol	Parameter	Test co	onditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E = 0)	V _{CB} = 250 V				5	μA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 6 V				5	μA
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage (I _B = 0)	l _C = 50 mA		250			V
V _{(BR)CBO}	Collector-base breakdown voltage (I _F = 0)	I _C = 100 μΑ		250			V
V _{(BR)EBO} ⁽¹⁾	Emitter-base breakdown voltage ($I_C = 0$)	l _E = 1 mA		6			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 8 A	I _B = 800 mA			3	V
V _{BE} ⁽¹⁾	Base-emitter voltage	I _C = 7 A	V _{CE} = 5 V			1.5	V
h	DC ourrent coin	I _C = 1 A	V _{CE} = 5 V	80		160	
h _{FE}	DC current gain	I _C = 7 A	$V_{CE} = 5 V$	35			
f _T	Transition frequency	I _C = 1 A	$V_{CE} = 5 V$		25		MHz

1. Pulsed duration = 300 $\mu s, \, duty \, cycle \leq 1.5\%$

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Figure 4.

DG11570

I_{S/B}

Electrical characteristics (curves) 2.1

Output characteristics

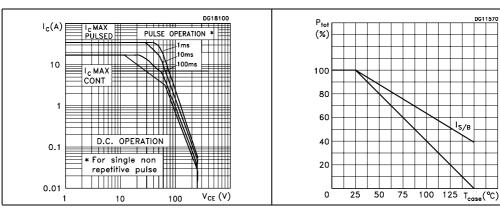


Figure 2. Safe operating area

Figure 3. **Derating curve**



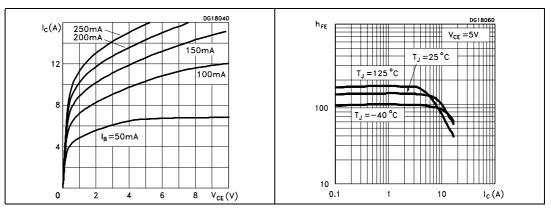
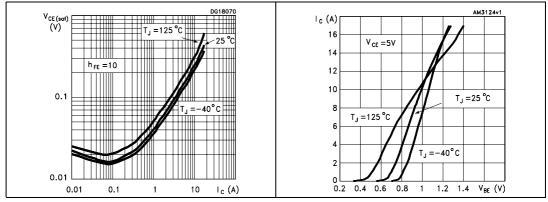


Figure 6. **Collector-emitter saturation** Figure 7. **Base-emitter voltage** voltage

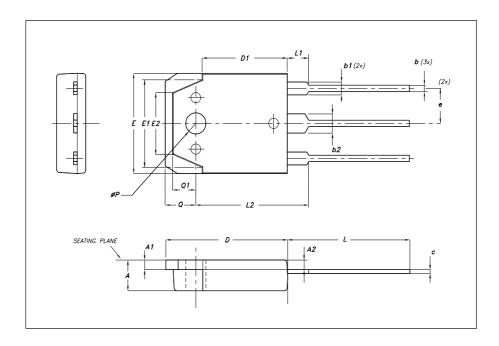


3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



	TO-3P Mechanical data			
DIM.	mm.			
	MIN.	ТҮР	MAX.	
A	4.6		5	
A1	1.45	1.50	1.65	
A2	1.20	1.40	1.60	
b	0.80	1	1.20	
b1	1.80		2.20	
b2	2.80		3.20	
с	0.55	0.60	0.75	
D	19.70	19.90	20.10	
D1		13.90		
E	15.40		15.80	
E1		13.60		
E2		9.60		
e	5.15	5.45	5.75	
L	19.50	20	20.50	
L1		3.50		
L2	18.20	18.40	18.60	
P	3.10		3.30	
Q		5		
Q1		3.80		





4 Revision history

Table 5. Document revision history

Date	Revision	Changes
26-Nov-2007	1	Initial release.
06-May-2008	2	New graphics
11-Jul-2008	3	Updated Figure 7.
17-Nov-2008	4	Content reworked to improve readability, no technical changes



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